

Download Pic24f Family Reference Manual

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Microchip Technology PIC24F Reference Manual

Page 10 PIC24F Family Reference Manual 66.3.2 Gain Control The Programmable Gain Amplifier (PGA) block can be used to amplify very low signals, but the differential input range of the SD A/D module should not be exceeded to avoid a saturated output result code. Page 11 SDOSR setting for the required output data rate, see Table 66-3.

PIC24F Family Reference Manual, Sect. 27 USB On

PIC24F Family Reference Manual 27.3.1.3.2 VBUS Control VBUS is the 5V USB power supplied by the host or a hub to operate bus-powered devices. The need for VBUS control depends on the role of the application.

PIC24F Family Ref. Manual, Section 35. Output Compare with ...

Section 35. Output Compare with Dedicated Timer Output Compare with Dedicated Timer 35 35.2 OUTPUT COMPARE REGISTERS Each output compare channel is comprised of the following registers: • OCxCON1 and OCxCON2 – Control registers for the output compare channel • OCxR – Data register for the output compare channel

PIC24F Family Reference Manual | Input/Output | Timer

PIC24F Family Reference Manual 15.1 INTRODUCTION This section describes the input capture module and its associated operational modes. The input capture module is used to capture a timer value from one of two selectable time bases upon an event on an input pin.

Section 14. Timers

Section 14. Timers Timers 14 14.2 TIMER VARIANTS All 16-bit timers available on the PIC24F devices are functionally identical with certain exceptions. The 16-bit timers are classified into three functional types; Type A timers, Type B timers and Type C timers. 14.2.1 Type A Timer At least one Type A timer is available on most PIC24F devices.

Section 17. 10

The PIC24F 10-bit A/D Converter has the following key features: • Successive Approximation Register (SAR) Conversion • Conversion Speeds of up to 500 ksp/s • Up to 16 External Analog Input Channels • Multiple Internal Reference Input Channels (select devices only) • External Voltage Reference Input Pins

MicroControllers

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Section 24. Inter

PIC24F Family Reference Manual DS39702A-page 24-2 Advance Information © 2006 Microchip Technology Inc. 24.1 OVERVIEW The Inter-Integrated Circuit (I²C) module is a serial interface useful for communicating with other peripheral or microcontroller devices. These peripheral devices may be serial EEPROMs, display drivers, A/D converters, etc.